

#### **Feature**

- This module is designed very compactly,Because diode Module and thyristor are put together.
- This module is also isolated type between electorode Terminal and mounting base.

$I_D$	100A		
$V_{RRM}$	800/1600V		
$I_{FSM}$	1.19/1.2	KA	
I <sup>2</sup> t	7030	$A^2S$	

### **Typical application**

- Inverter for AC or DC motor control
- Current stabilized power supply
- Switching power supply

### DIODE

Maximum Ratings

(T<sub>J</sub> =25°C)

Symbol	Itama	Ratings		Unit
	ltem	HDFA100BA80	HDFA100BA160	Offit
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	800	1600	V
V <sub>RSM</sub>	Non-Repetitive Peak Reverse Voltage	960	1700	V

Symbol	ltem	Conditions	Ratings	Unit
I <sub>D</sub>	Output Current (D.C.)	Three phase full wave, T <sub>C</sub> =117°C	100	Α
I <sub>FSM</sub>	Surge forward current	50/60Hz,peak value, non-repetitive	1186/1300	Α
TJ	Operating Junction Temperaturea		-40 to +150	$^{\circ}\!\mathbb{C}$
T <sub>stq</sub>	Storage Temperature		-40 to +125	$^{\circ}\mathbb{C}$
V <sub>iso</sub>	Isolation Breakdown Voltage (R.M.S.)	R.M.S,t=1min,I <sub>iso:</sub> 1mA(max)	2500	V
F <sub>M</sub>	Mounting (M5)		2.7	N-m
Wt	Mass		150	g

#### ■ Electrical Characteristics

Symbol	ltem	Conditions	Ratings	Unit
I <sub>RRM</sub>	Repetitive Peak Reverse Current,max.	$T_J = 150 ^{\circ}\text{C}$ , $V_{RM} = V_{RRM}$	12	mA
V <sub>FM</sub>	Forward Voltage Drop,max.	T」=25℃,IF=50A	1.30	V
R <sub>th(j-c)</sub>	Thermal Impedance, max.	Junction to Case (TOTAL)	0.20	°C/W
R <sub>th(c-f)</sub>	Thermal Impedance, max.	Case to Fin	0.10	°C/W



### • THYRISTOR

# Maximum Ratings

(T<sub>J</sub> =25°C)

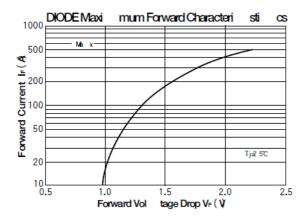
Symbol	ltem	Ratings		Unit
		HDFA100BA80	HDFA100BA160	Unit
$V_{RRM}$	Repetitive Peak Reverse Voltage	800	1600	V
V <sub>RSM</sub>	Non-Repetitive Peak Reverse Voltage	960	1700	V
$V_{DRM}$	Repetitive Peak off-State Voltage	800	1600	V

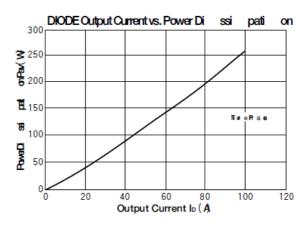
Symbol	ltem	Conditions	Ratings	Unit
I <sub>T(AV)</sub>	Average On-State Current	Singl phase half wave $.180^{\circ}$ conduction, $$T_{C}\!\!=\!\!85^{\circ}\!C$	100	А
I <sub>TSM</sub>	Surge On-State Current	peak value, non-repetitive,50/60Hz	1186/1300	Α
I <sup>2</sup> t	I <sup>2</sup> t		7030	A <sup>2</sup> S
di/dt	Critical Rate of Rise of On-State Current	$I_G\!\!=\!\!100\text{mA} \ _{,}\!V_D\!\!=\!\!1/2V_{\text{DRM}}$	150	A/us
V <sub>iso</sub>	Isolation Breakdown Voltage (R.M.S.)	R.M.S,t=1min,I <sub>iso:</sub> 1mA(max)	2500	V
TJ	Operating Junction Temperature		-40 to +135	$^{\circ}$
T <sub>stq</sub>	Storage Temperature		-40 to +125	${\mathbb C}$
F <sub>M</sub>	Mounting (M5)		2.7	N-m
W <sub>t</sub>	Mass		150	g

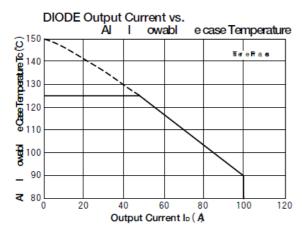
#### **■** Electrical Characteristics

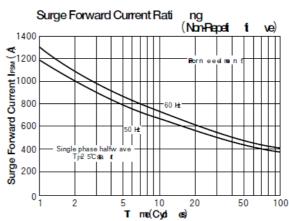
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I <sub>DRM</sub>	Repetitive Peak Off-State Current,max	$T_J = 135 ^{\circ}\text{C}$ , $V_D = V_{DRM}$	70	mA
I <sub>RRM</sub>	Repetitive Peak Reverse	T <sub>J</sub> =135°C ,V <sub>D=</sub> V <sub>RRM</sub>	70	mA
IRRM	Current,max.	1 J - 133 C, V D= V RRM	70	1117
V <sub>TM</sub>	Peak On-State	T <sub>J</sub> =125℃,I <sub>TM</sub> =50A	1.20	V
VIM	Voltage,max		1.20	, v
$I_{GT}$	Gate Trigger Current,max	V <sub>D</sub> =6V,I <sub>A</sub> =1A	70	mA
$V_{GT}$	Gate Trigger Voltage,max.		3	V
dr./d+	Critical Rate of Rise of	$T_{J}=125^{\circ}C, V_{DM}=0.67V_{DRM}$	500	V/us
dv/dt	Off-State Voltage,min.		300	v/us
R <sub>th(j-c)</sub>	Thermal Impedance, max.	Junction to Case	0.36	°C/W
R <sub>th(c-f)</sub>	Thermal Impedance, max.	Case to Fin	0.10	°C/W

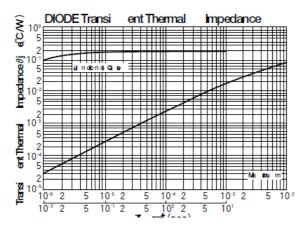


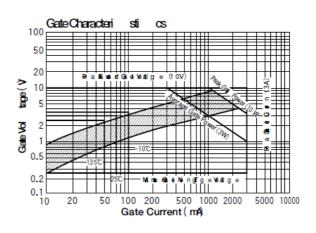


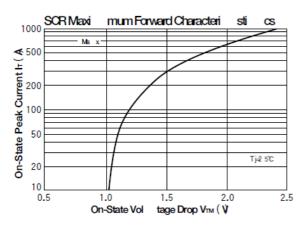


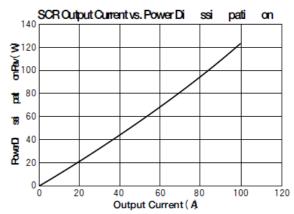




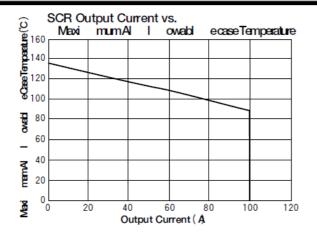


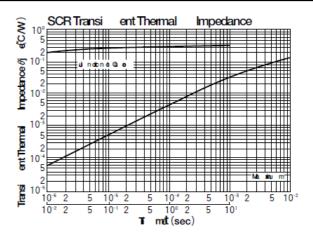




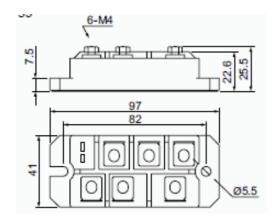








# **Outline:**



# **Circuit Drawing:**

